



06-26-02 2154

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit 2784

Examiner Larry Donaghue

In Reply, Please Refer to:  
Case: P3803  
Serial No.: 09/312,302 ✓  
Filed: 05/14/1999  
Subject: Interrupt and Exception Handling for Multi-Streaming Digital Processors

To: The Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Dear Sir,

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JUL 03 2002

Technology Center 2100

**Information Disclosure Statement Under 37 C.F.R. 1.97**

Enclosed herewith is an Information Disclosure Statement (IDS) by Applicant listing two references received from the PCT International Search Report (copy enclosed) in case PCT/US00/06621, mailed August 29, 2000. Applicant desires that the reference be entered in the above referenced case as relevance dictates.

As prosecution has not begun in this case, in accordance with rule 1.97(a)(3), no additional fee is required. Please charge any deficiency to Deposit Account No. 50-0534.

Respectfully submitted,

Mario D. Nemirovsky et al.

by

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